

IN THE CLAIMS

Please amend the claims as follows:

1. (Currently Amended): A drive method for an EL display panel, the EL display panel comprising:

EL elements arranged in a matrix;

driver transistors which supply current to be passed through the EL elements;

first switching elements placed in current paths of the EL elements; and

a gate driver circuit which turns on and off the first switching elements for control;

[[and]]

~~a source driver circuit which supplies programming current to the driver transistors,~~

~~wherein the driver transistors are p-channel transistors,~~

~~unit transistors which generate the programming current in the source driver circuit~~

~~are n-channel transistors, and~~

wherein:

the gate driver circuit turns on and off the first switching elements ~~at least~~ two or more times during one frame period ~~or one field period, and~~

an image signal applied to each pixel is retained only once during the one frame period.

2. (Currently Amended): A drive method for an EL display panel, the EL display panel comprising:

EL elements arranged in a matrix;

driver transistors which supply current to be passed through the EL elements;

first switching elements placed in current paths of the EL elements; and

a gate driver circuit which turns on and off the first switching elements for control;

wherein the gate driver circuit keeps the first switching elements off two horizontal scanning periods during one frame period, and

an image signal is written into the pixel once during the one frame period.

~~The drive method for an EL display panel according to claim 1, wherein the first switching elements are turned off periodically during one frame period or one field period.~~

3. (Currently Amended): A drive method for an EL display panel, the EL display panel comprising:

EL elements arranged in a matrix;

driver transistors which supply current to be passed through the EL elements;

first switching elements placed in current paths of the EL elements;

a gate driver circuit which turns on and off the first switching elements for control;

and

a source driver circuit which supplies programming current to the driver transistors,

~~wherein the driver transistors are p-channel transistors,~~

~~unit transistors which generate the programming current in the source driver circuit are n-channel transistors,~~

wherein:

a period during which a pixel row is selected and programmed with current is constructed from a first period and second period,

a first current is applied during the first period,

a second current is applied during the second period,

the first current is larger than the second current, and

the source driver circuit outputs the first current during the first period and outputs the second current during the second period which comes after the first period.

4. (Currently Amended): The drive method for the EL display panel according to claim 1,

wherein the first switching elements are turned off periodically during one frame period ~~or one field period.~~

5. (Currently Amended): An EL display panel, comprising:

a source driver circuit which outputs ~~an image signal programming current;~~

EL elements arranged in a matrix;

driver transistors which supply current to be passed through the EL elements;

first switching elements placed in current paths of the EL elements;

second switching elements which constitute paths used to transmit ~~programming~~

~~current the image signal~~ to the driver transistors;

a first gate driver circuit which turns on and off the first switching elements for control; and

a second gate driver circuit which turns on and off the second switching elements for control;

~~a source driver circuit which supplies programming current to the driver transistors;~~

~~wherein the driver transistors are p-channel transistors;~~

~~unit transistors which generate the programming current in the source driver circuit are n-channel transistors;~~

wherein:

the first gate driver circuit turns off the first switching elements a number of times during one frame period ~~or one field period, and~~

~~the first gate driver circuit is placed or formed on one side of the display panel, and~~

~~the second gate driver circuit is placed or formed on another side of the display panel~~  
an image signal applied to each pixel is retained only once during the one frame  
period.

6. (Currently Amended): The EL display panel according to claim 5, wherein the first and second gate driver circuits are formed in a same process as the driver transistors and the source driver circuit is made of a semiconductor chip.

7. (Currently Amended): An EL display panel, comprising:  
gate signal lines;  
source signal lines;  
a source driver circuit which outputs an image signal programming current;  
a gate driver circuit;  
EL elements arranged in a matrix;  
driver transistors which supply current to be passed through the EL elements; and  
first transistors placed in current paths of the EL elements;  
second transistors which constitute paths used to transmit an image signal  
programming current to the driver transistors; and  
~~a source driver circuit which supplies programming current to the driver transistors,~~  
~~wherein the driver transistors are p-channel transistors,~~  
~~unit transistors which generate the programming current in the source driver circuit~~  
~~are n-channel transistors,~~  
~~the source driver circuit outputs programming current to the source signal lines,~~  
wherein:

the gate signal lines are ~~driver circuit is~~ connected to the gate driver circuit signal lines,

gate terminals of the second transistors are connected to the gate signal lines,  
source terminals of the second transistors are connected to the source signal lines, and  
~~drain terminals of the second transistors are connected to drain terminals of the driver~~  
~~transistors, and~~

the gate driver circuit selects a plurality of gate signal lines and supplies the image signal programming current to the driver transistors of a plurality of pixel rows pixels.

8. (Currently Amended): An EL display panel, comprising:

a display area in which pixels having an EL element are arranged in a matrix; ~~efI~~  
~~pixel rows (I is an integer larger than 1) and J pixel columns (J is an integer larger than 1);~~

a dummy pixel row formed outside the display area,

a source driver circuit which applies an image signal to source signal lines connected to pixels in the display area and to pixels in the dummy pixel row;

a gate driver circuit which applies a turn-on voltage or turn-off voltage to gate signal lines connected to pixels in the display area and to pixels in the dummy pixel row; and

a dummy pixel row formed outside the display area,

~~wherein EL elements are arranged in a matrix in the display area and emit light based~~  
~~on the image signal from the source driver circuit, and~~

wherein the dummy pixel row either does not emit light or emits light not visible to the eye.

9. (Currently Amended): The EL display panel according to claim 7,

wherein the gate driver circuit selects a plurality of pixel rows at a time and applies the image signal from the source driver circuit to the plurality of pixel rows[[; and]]  
~~a dummy pixel row is selected when the first pixel row or 1-th pixel rows is selected.~~

10. (Previously Added): The EL display panel according to claim 7, wherein the gate driver circuit is constructed of p-channel transistors.

11. (Currently Amended): An EL display panel, comprising:  
EL elements arranged in a matrix;  
driver transistors which supply current to be passed through the EL elements;  
first switching elements placed in current paths of the EL elements;  
a gate driver circuit which turns on and off the first switching elements for control;  
and

a source driver circuit which supplies programming current to the driver transistors,  
wherein:  
the gate driver circuit keeps the first switching elements off for two horizontal  
scanning periods during one frame period, and  
an image signal applied to each pixel is retained only once during the one frame  
period the driver transistors and the first switching elements are p-channel transistors, and  
unit transistors which generate the programming current in the source driver circuit  
are n-channel transistors.

12. (Currently Amended): A drive method for an EL display panel in which pixels having respective EL elements are arranged in a matrix, comprising the steps of: supplying EL elements with a current which makes the EL elements emit light brighter than a predetermined brightness; and making the EL elements emit light for a period equal to  $1/N$  of one frame period or one field period ( $N$  is a real number larger than 1).

13. (Previously Added): The drive method for the EL display panel according to claim 12, wherein the period equal to  $1/N$  of a frame is divided into a plurality of periods.

14. (Currently Amended): A drive method for an EL display panel in which pixels having respective EL elements are arranged in a matrix ~~which uses a current to program currents to be passed through EL elements~~, comprising the steps of: making the EL elements emit light brighter than a predetermined brightness; displaying a display area equal to  $1/N$   $[(N>1)]$  ( $N$  is a real number larger than 1) of an entire screen; and shifting the display area of  $1/N$  of the entire screen in sequence to display the entire screen.

15. (Currently Amended): An EL display apparatus comprising: the [[an]] EL display panel according to claim 5 comprising EL elements arranged in a matrix; ~~driver transistors which supply current to be passed through the EL elements;~~ ~~first switching elements placed in current paths of the EL elements;~~ and

~~a gate driver circuit which turns on and off the first switching elements~~, and a receiver.